## **CERTIFICATE UNDER 37 C.F.R. § 3.73(b)**

Applicar	nt: I	Thomas H. White, William Bradley Vest, Dirk Alan Reese, and Myron Wai Wong						
Applicat	ion l	No.: <u>Unassigned</u> Filed: <u>Herewith</u>						
	TECHNIQUES FOR CONFIGURING PROGRAMMABLE LOGIC USING ON-CHIP NONVOLATILE MEMORY							
		oration, a Corporation						
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В. 🗌	A chain of title from the inventor(s), of the patent application identified above, to the current assignee as shown below:							
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Date:		3/4/04						
Name:		Derek E. Minihane						
Title:								
Signatı	ure:	0154/1						

PTO/SB/81 (02-01)

Approved for use through 10/31/2002. OMB 0651-0035
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AUTHORIZATION OF AGEN	T

Application Number	Unassigned		
Filing Date	Herewith		
First Named Inventor	Thomas H. White		
Title	TECHNIQUES FOR CONFIGURING PROGRAMMABLE LOGIC USING ON-CHIP NONVOLATILE MEMORY		
Group Art Unit	Unassigned		
Examiner Name	Unassigned		
Attorney Docket Number	15114-070200US		

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Assignee of record of the entire interest. See 37 CFR 3.71.									
Statement under 37 CFR 3.73(b) is	s enclosed. (Form PTC	D/SB/96).							
SIGNATURE of Applicant or Assignee of Record									
Name Derek E. Minihane – Director, Intellectual Property Law and Assistant Secretary									
Signature 1) ( S 4 1 (									
Date 3/4/64									
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.									
★ Total of 1 forms are submitted.									

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